

Hardware-in-the-Loop Testing (glossary)

hardware-in-the-loop testing

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A type of dynamic simulation that includes one or more actual system components operating in conjunction with simulated components. (Kossiakoff and Sweet 2003, 449)

Sources

Kossiakoff, A., and W. N. Sweet, 2003. *Systems Engineering: Principles and Practice*, Hoboken, NJ: J. Wiley & Sons.

Discussion

None.

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